

ABSTRACT

System and method for processing HDTV format video signals have been disclosed. A disclosed embodiment comprises a HDTV timing generator having as inputs a vertical sync and a horizontal sync. The HDTV timing generator outputs a digital HD level signal. The disclosed embodiment further comprises a DAC interface. The DAC interface can include an encoder channel, or more than one encoder channel. The encoder channel can receive a digital HD level signal, a SCART level signal, an NTSC level signal, a PAL level signal, and a SECAM level signal. The encoder channel can further receive a HDTV format data input, a SCART format data input, an NTSC format data input, a PAL format data input, and a SECAM format data input. The output of the DAC interface can be coupled to a DAC which in turn generates an output suitable for display on a monitor.

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